

# HP3456A

## Quick Reference Card

Martin Hepperle, June 2016

### Commands

|  |   |
|--|---|
| <b>T&lt;1, 2, 3, or 4&gt;</b>                | Trigger<br>T1 = Internal trigger<br>T2 = External<br>T3 = Software single trigger<br>T4 = Trigger and hold  |
| <b>Z&lt;0, or 1&gt;</b>                      | Autozero<br>Z0 = OFF      Z1 = ON   |
| <b>FL&lt;0, or 1&gt;</b>                     | Filter<br>FL0 = OFF      FL1 = ON   |
| <b>TE&lt;0, or 1&gt;</b>                     | Test<br>TE0 = OFF      TE1 = ON   |
| <b>Registers</b>                             | ST = Store<br>RE = recall<br>N = Number of readings<br>G = Number of digits displayed<br>I = number of power lines integrated<br>D = Settling delay in seconds<br>M = Mean register in statistics mode (Read-Only)<br>V = Variance register in statistics mode (Read-Only)<br>C = Count register in statistics mode (Read-Only)<br>L = Lower register for pass/fail<br>R = R Register<br>U = Upper register for pass/fail<br>Y = Y Register<br>Z = Z Register |
| <b>M&lt;0, 1, 2, 3, 4, 5, 6, 7, or 8&gt;</b> | Math<br>M0 = OFF<br>M1 = Pass/Fail<br>M2 = Statistics      (Mean, Variance, Count)<br>M3 = Null<br>M4 = Thermistor      (°F)<br>M5 = Thermistor      (°C)<br>M6 = Scale      ((X-Z)/Y)<br>M7 = % Error      ((X-Y)/Y x 100)<br>M8 = dB      (20 Log X/Y)  |
| <b>RS&lt;0, or 1&gt;</b>                     | Reading Storage<br>RS0 = OFF      RS1 = ON  |
| <b>SO&lt;0, or 1&gt;</b>                     | System Output Mode<br>SO0 = OFF      SO1 = ON   |
| <b>D&lt;0, or 1&gt;</b>                      | Display<br>D0 = OFF      D1 = ON  |
| <b>P&lt;0, or 1&gt;</b>                      | Packed Output Format<br>P0 = OFF      P1 = ON   |
| <b>CL1</b>                                   | Clear – Continue Active   |
| <b>W</b>                                     | Number separator character  |
| <b>H</b>                                     | Home – Software Reset   |
| <b>SW1</b>                                   | Front/Rear Panel Switch Sense<br>returns 0 or 1   |
| <b>O&lt;0, or 1&gt;</b>                      | EOI<br>O0 = OFF      O1 = ON  |
| <b>L1</b>                                    | Store program: Begin  |

|           |  |
|-----------|--|
| <b>Q</b>  | Store program: End   |
| <b>X1</b> | Execute stored program   |
| <b>SM</b> | Define Service Request Mask (3 octal digits)<br>001 = Front panel SQR button<br>002 = Program memory execution complete<br>004 = Data ready<br>010 = Trigger too fast<br>020 = Illegal instrument state, internal error, syntax error<br>040 = Program memory error<br>100 = Service request<br>200 = Limits failure |

### Examples (spaces for clarity only, can be omitted)

|   |                          |
|---|--------------------------|
| Switch to DCV, autorange, no Math, hold trigger   | "F1 R1 M0 T4"            |
| Switch to DCV and store "10" in register "N". Here a space or a "W" is required to separate the "1" from the following "10" | "F1 10STN"<br>"F1W10STN" |
| Set SRQ Mask to SRQ on data ready (argument in octal)   | SM004                    |

### P0: ASCII Output (7 Digits plus Exponent)

14 bytes: +RDDDDDDDD+D[CR][LF]

- R is a digit which indicates over-range if R=1 else R=0
- The decimal point is floating in the eight digits field between R and +.
- If the number of readings per trigger is greater than one, the readings are separated by a comma and the [CR][LF] is only added to the end of the sequence. The EOI line is set for the last byte, if not disabled to speed up the transfer.

### P1: Packed Output (6 BCD digits plus Exponent)

4 bytes: +DDDD+OR DDDD.DDDD DDDD.DDDD DDDD.DDDD

Byte 1: +DDDD+OR (+DDDD represents the exponent, + the sign, OR the over range bit)  
 Byte 2: DDDD DDDD (digits 1 and 2 in BCD code)  
 Byte 3: DDDD DDDD (digits 3 and 4 in BCD code)  
 Byte 4: DDDD DDDD (digits 5 and 6 in BCD code)

- The decimal point is implicitly placed at the OR bit, i.e. the result is normalized.
- The EOI line is set for the last byte, if not disabled to speed up the transfer.
- If the number of readings per trigger is greater than one, the readings are sent without separator and the [EOI] line is only asserted when the last byte is sent, if not disabled.

|           | Function        |                      | Range Codes |             |             |              |               |             |              |               |                |
|-----------|-----------------|----------------------|-------------|-------------|-------------|--------------|---------------|-------------|--------------|---------------|----------------|
|           | S0              | S1                   | R1          | R2          | R3          | R4           | R5            | R6          | R7           | R8            | R9             |
| <b>F1</b> | DCV             | DCV Ratio            | Auto        | 100mV       | 1000mV      | 10V          | 100V          | 1000V       | *            | *             | *              |
| <b>F2</b> | ACV             | ACV Ratio            | Auto        | 100mV       | 1000mV      | 10V          | 100V          | 1000V       | *            | *             | *              |
| <b>F3</b> | ACV+DCV         | ACV+DCV Ratio        | Auto        | 100mV       | 1000mV      | 10V          | 100V          | 1000V       | *            | *             | *              |
| <b>F4</b> | 2-Wire $\Omega$ | O.C. 2-Wire $\Omega$ | Auto        | 1K $\Omega$ | 1K $\Omega$ | 10K $\Omega$ | 100k $\Omega$ | 1M $\Omega$ | 10M $\Omega$ | 100M $\Omega$ | 1000M $\Omega$ |
| <b>F5</b> | 4-Wire $\Omega$ | O.C. 4-Wire $\Omega$ | Auto        | 1K $\Omega$ | 1K $\Omega$ | 10K $\Omega$ | 100k $\Omega$ | 1M $\Omega$ | 10M $\Omega$ | 100M $\Omega$ | 1000M $\Omega$ |

\* indicates an invalid combination of function and range.

| Function (Shift)                   | S0                  | S1                             |
|------------------------------------|---------------------|--------------------------------|
| F1                                 | DCV                 | DCV / DCV Ratio                |
| F2                                 | ACV                 | ACV / DCV Ratio                |
| F3                                 | ACV+DCV             | ACV+DCV / DCV Ratio            |
| F4                                 | 2-Wire K-Ohms       | O.C. 2-Wire K-Ohms             |
| F5                                 | 4-Wire K-Ohms       | O. C. 4-Wire K-Ohms            |
| Range                              |                     |                                |
| R1                                 | Auto                |                                |
| R2                                 | 100 mV or 1K-Ohms   |                                |
| R3                                 | 1000 mV or 1K-Ohms  |                                |
| R4                                 | 10 V or 10 K Ohms   |                                |
| R5                                 | 100 V or 100 K Ohms |                                |
| R6                                 | 1000 V or 1 M Ohms  |                                |
| R7                                 | 10 M Ohms           |                                |
| R8                                 | 100 M Ohms          |                                |
| R9                                 | 1000 M Ohms         |                                |
| Trigger                            |                     |                                |
| T1                                 | Internal            | automatically repeating        |
| T2                                 | External            | by external hardware           |
| T3                                 | Single              | performs trigger when executed |
| T4                                 | Hold                | requires TRIGGER               |
| Auto Zero                          |                     |                                |
|                                    | OFF                 | ON                             |
|                                    | Z0                  | Z1                             |
| Filter                             |                     |                                |
|                                    | OFF                 | ON                             |
|                                    | FL0                 | FL1                            |
| Test                               |                     |                                |
|                                    | OFF                 | ON                             |
|                                    | TE0                 | TE1                            |
| Registers                          |                     |                                |
| Store                              | ST                  |                                |
| Recall                             | RE                  |                                |
| # of Readings                      | N                   |                                |
| # of Digits displayed              | G                   |                                |
| # of Power Line Cycles Integrated  | I                   |                                |
| Delay                              | D                   |                                |
| Mean Register (Read Only)          | M                   |                                |
| Variance Register (Read Only)      | V                   |                                |
| Count Register (Read Only)         | C                   |                                |
| Lower Register                     | L                   |                                |
| R Register                         | R                   |                                |
| Upper Register                     | U                   |                                |
| Y Register                         | Y                   |                                |
| Z Register                         | Z                   |                                |
| Math                               |                     |                                |
| OFF                                | M0                  |                                |
| Pass/Fail                          | M1                  |                                |
| Statistics (Mean, Variance, Count) | M2                  |                                |
| Null                               | M3                  |                                |
| Thermistor (°F)                    | M5                  |                                |
| Thermistor (°C)                    | M6                  |                                |
| Scale ((X-Z)/Y)                    | M7                  |                                |
| % Error ((X-Y)/Y x 100)            | M8                  |                                |
| dB (20 Log X/Y)                    | M9                  |                                |
| Reading Storage                    |                     |                                |
|                                    | OFF                 | ON                             |

|                               |                    |                         |
|-------------------------------|--------------------|-------------------------|
|                               | RS0                | RS1                     |
| <b>System Output Mode</b>     | <b>OFF</b>         | <b>ON</b>               |
|                               | S00                | S01                     |
| <b>Display</b>                | <b>OFF</b>         | <b>ON</b>               |
|                               | D0                 | D1                      |
| <b>Packed Output Format</b>   | <b>OFF (ASCII)</b> | <b>ON</b>               |
|                               | P0                 | P1                      |
| Clear-Continue Active         | CL1                |                         |
| Number Separator              | W                  |                         |
| Home (Software Reset)         | H                  |                         |
| Front/Rear Panel Switch Sense | SW1                | (returns 0 or 1)        |
| <b>EOI</b>                    | <b>Disable</b>     | <b>Enable</b>           |
|                               | 00                 | 01                      |
| <b>Program Memory</b>         |                    |                         |
| Load Program ON (Syntax)      | L1                 | starts program sequence |
| Load Program OFF (Syntax)     | Q                  | end program sequence    |
| Execute Program               | X1                 |                         |
| Service Request Mask          | SM                 |                         |

### Examples (spaces for clarity only, can be omitted)

|   |                          |
|---|--------------------------|
| switch to DCV, autorange, no Math, hold trigger   | "F1 R1 M0 T4"            |
| switch to DCV and store "10" in register "N". Here a space or a "W" is required to separate the "1" from the following "10" | "F1 10STN"<br>"F1W10STN" |
| set SRQ Mask (argument in octal)  | SM004                    |

### ASCII Output (P0) 14 bytes per measurement

14 bytes: +RDDDDDDD+D[CR][LF]

- R is a digit which indicates over-range if R=1 else R=0
- The decimal point is floating in the seven digits field DDDDDDDD
- If the number of readings per trigger is greater than one, the readings are separated by a comma and the [CR][LF] is only added to the end of the sequence. The EOI line is set for the last byte, if not disabled to speed up the transfer.

### Packed Output (P1) 4 bytes per measurement

bits in byte 1: +DDDD+OR (+DDDD represents the exponent, + the sign, OR the over range bit)

bits in byte 2: DDDD DDDD ( digits 1 and 2 in BCD code)

bits in byte 3: DDDD DDDD ( digits 3 and 4 in BCD code)

bits in byte 4: DDDD DDDD ( digits 5 and 6 in BCD code)

- The decimal point is implicitly placed at the OR bit, i.e. the result is normalized.
- The EOI line is set for the last byte, if not disabled to speed up the transfer.
- If the number of readings per trigger is greater than one, the readings are sent without separator and the [EOI] line is only asserted when the last byte is sent, if not disabled to speed up the transfer.